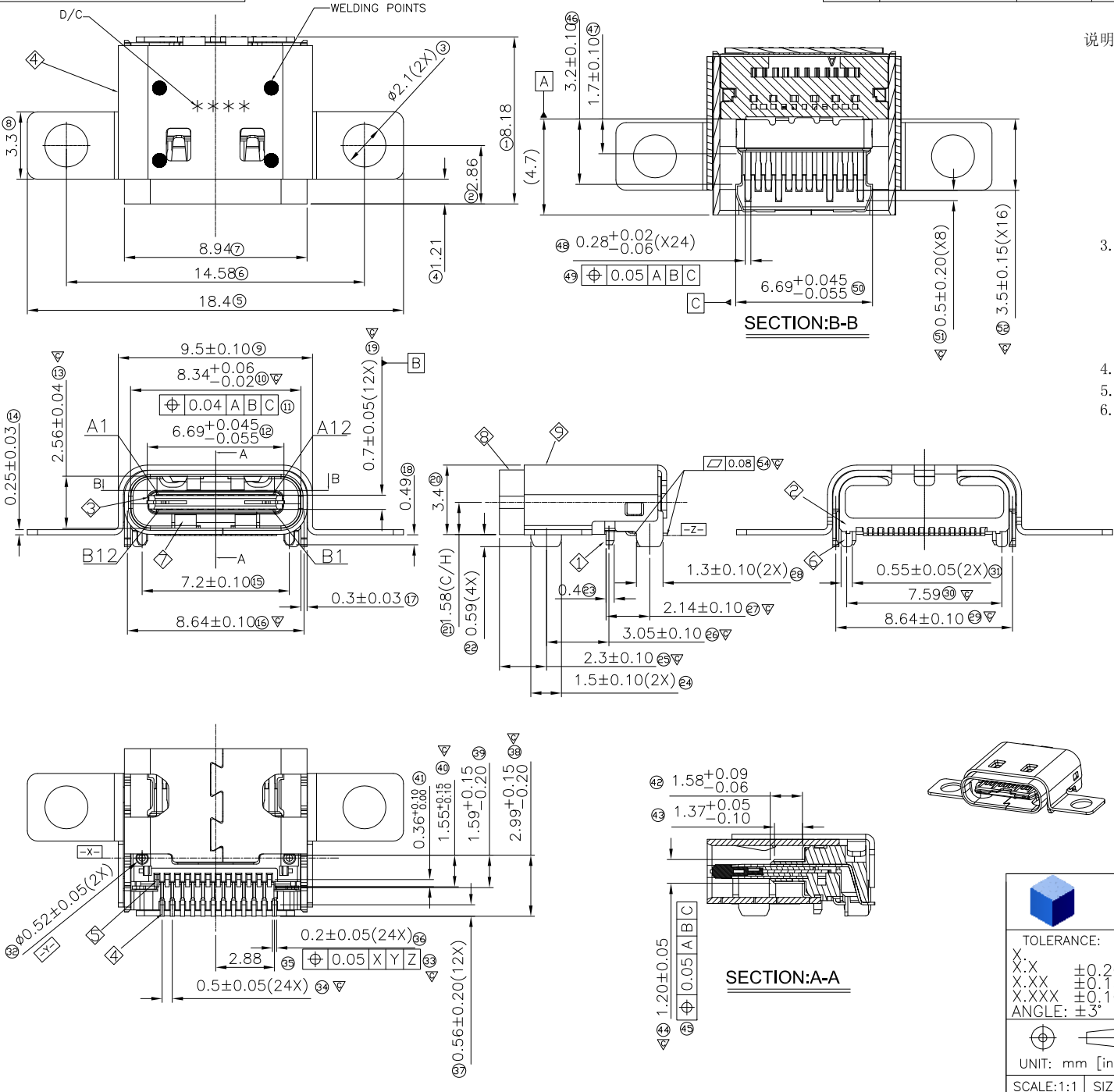


# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2017/07/28	Phebe Su



**说明:**

- 物理性能:
  - 1-1: 材质: SEE TABLE
- 电气性能:
  - 2-1: 接触电流额定值: V B V S 和 G N D 接触电流: 1.25A; 总的平均和: 5 A
  - 2-2: 接触电阻值: 40 mΩ Max、对 V B U S、G N D 和与其它接触 50 mΩ Max、
  - 2-3: 耐压: 100VAC(RMS) Min
  - 2-4: 绝缘电阻: 100mΩ Min
- 机械性能:
  - 3-1: 插入力: 5~20 N
  - 3-2: 拔出力: 10000次循环插拔: 8~20 N  
循环测试10000次后: 6~20 N
  - 3-3: 寿命: 10000 次
  - 3-4: 高低环境温度: -5 5 ° C ~ +8 5 ° C
- 标记 "⊙" 是FAI尺寸
- 标记 "▽" 是首件检验尺寸
- D/C: \*\*\*\*  
年 周

MATRIX PART NO:  
**MUSB12-30-243**  
 MATRIX USB Pin Number Series number  
 Plating  
 01:Gold Flash  
 15:15u"  
 30:30u"

序号	名称	规格	电镀
⊙	外壳	SUS304, 1/2H T=0.25mm	整体镀黑镍, 镍底80~120u"MIN
⊙	内壳	SUS304, 1/2H T=0.30mm	整体镀黑镍, 镍底80~120u"MIN
◇	中夹片	SUS304, 1/2H T=0.10mm	素材点焊
⊙	中钢片	SUS301 1/2H T=0.15mm	整体镀镍80~120u"MIN
⊙	SMT短端子	C18400 R480 T=0.12mm	接触区镀金30u"Min, 焊接区镀金6u", 镀底80~120u"MIN
⊙	SMT长端子	C18400 R480 T=0.12mm	接触区镀金30u"Min, 焊接区镀金6u", 镀底80~120u"MIN
◇	中钢片Molding	LCP E130i HF 黑色	
◇	后贴Molding	LCP E130i HF 黑色	
◇	前贴Molding	LCP E130i HF 黑色	

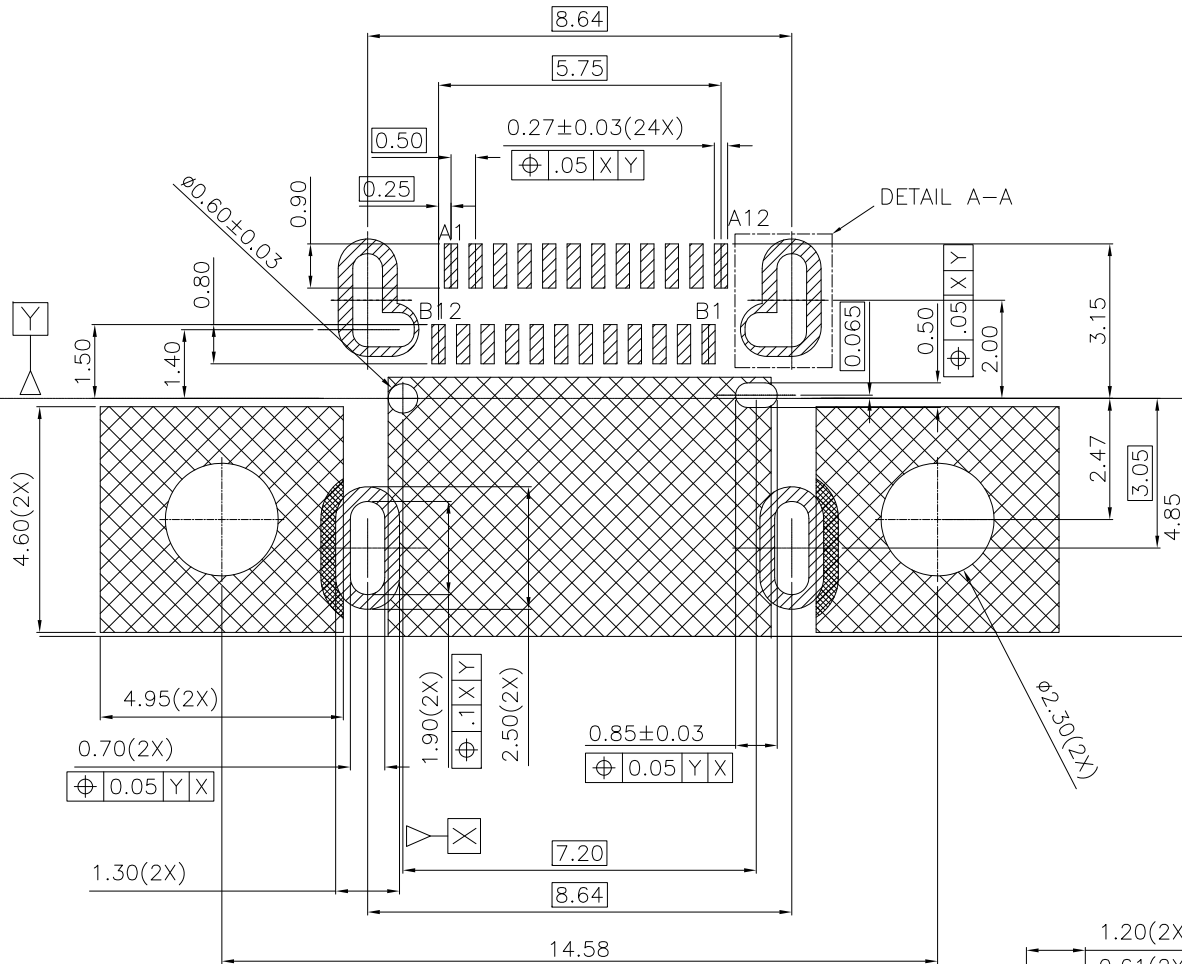


**Matrix Electronics Co.,Ltd**

TOLERANCE: X.X X.XX X.XXX ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2017/07/28	PART NAME: USB 3.1 Type C Gen 2 Female R/A, CL 1.58
APPROVED BY1: Richard Hsieh	DATE : 2017/07/28	PART NO.	MUSB12-30-243
APPROVED BY2: Richard Hsieh	DATE : 2017/07/28	MOLD NO.	NA
SCALE:1:1	SIZE:A4	DRAW NO.	SHEET NO. 1 OF 2

# GP Component

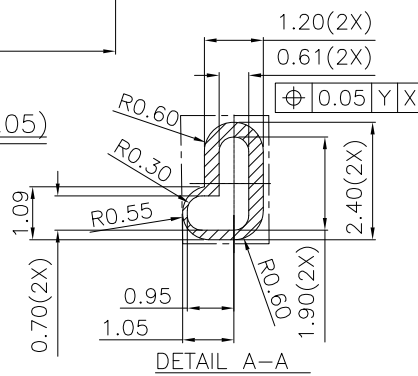
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2017/07/28	Phebe Su



USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	Vbus	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use(SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair—Position 1	B7	Dn2	Positive half of the USB 2.0 differential pair—Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair—Position 1	B6	Dp2	Negative half of the USB 2.0 differential pair—Position 2
A8	SBU1	Sideband Use(SBU)	B5	CC2	Configuration Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

RECOMMEND PCB LAYOUT(TOLERANCE:±0.05)  
(THICKNESS:1.0±0.05mm)



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.20 X.XX ±0.15 X.XXX ±0.10 ANGLE: ±3°	DESIGN BY :	DATE :	PART NAME:	
	Phebe Su	2017/07/28	USB 3.1 Type C Gen 2 Female R/A, CL 1.5B	
	CHECKED BY:	DATE :	PART NO.	MUSB12-30-243
APPROVED BY1:		DATE :	MOLD NO.	NA
APPROVED BY2:		DATE :	DRAW NO.	
SCALE:1:1	SIZE:A4	Richard Hsieh	2017/07/28	SHEET NO. 2 OF 2