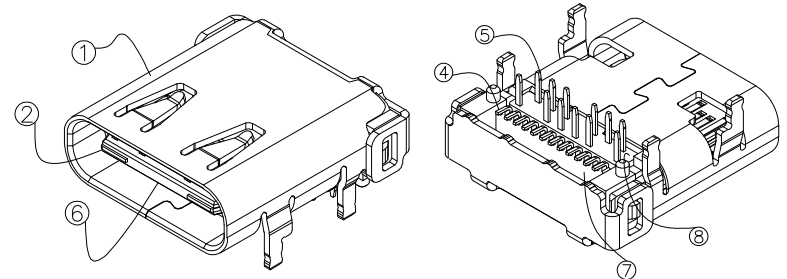
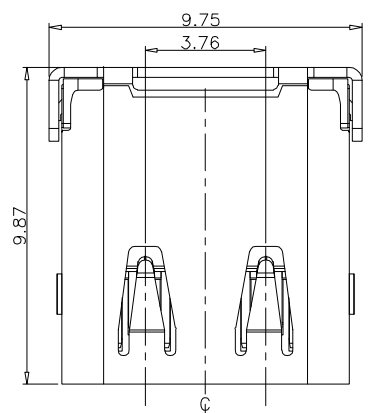


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/04/13	Phebe Su

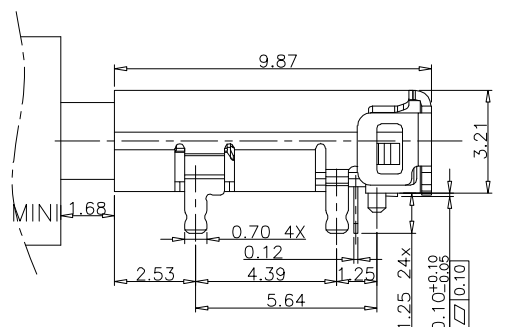
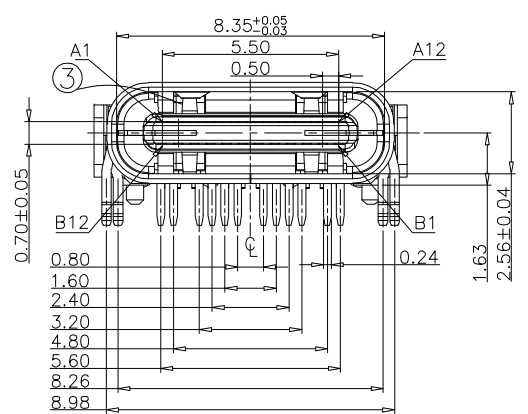


ELECTICAL (电气性能) :

- CONTACT RESISTANCE : 20 Milliohm MAX.
接触电阻: 20 Milliohm MAX.
- DIELECTRIC WITHSTANDING VOLTAGE: 100V AC AT SEA LEVEL
耐电压: 100V AC AT SEA LEVEL
- INSULATION RESISTANCE: 100 Megohms MIN.
绝缘电阻: 100 Megohms MIN

MECHANICAL (机械性能) :

- MATING FORCE: 5-20N MAX.
插入力: 5-20N MAX.
- UNMATING FORCE: 10-20N MIN.
拔出力: 10-20N MIN
- DURABILITY: 10000 CYCLES
耐久: 10000次

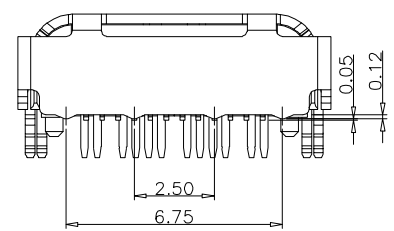
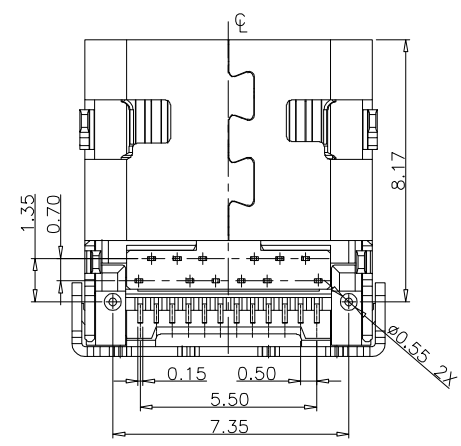


MATRIX PART NO:



MATRIX USB MUSB 12 - 01 - 104 Series number

Pin Number

Plating
01:Gold Flash
15:15u"
30:30u"

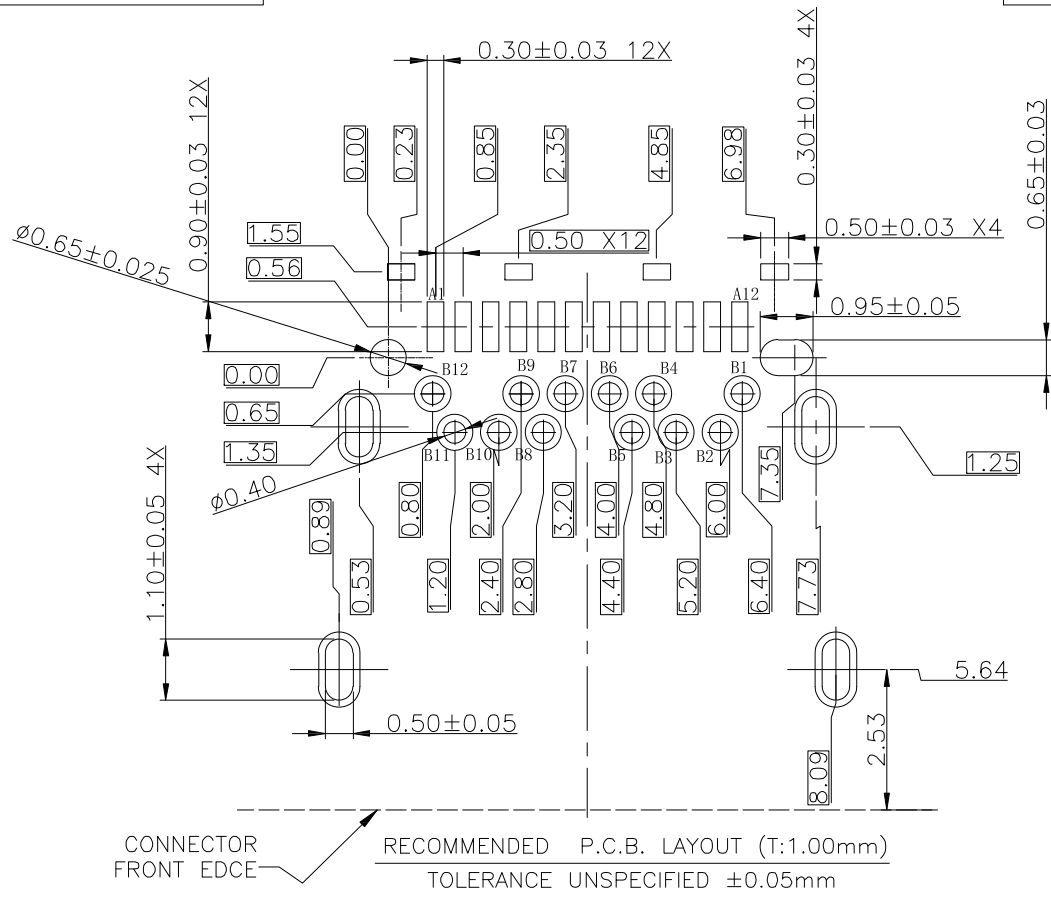


1	THERMOPLASTIC UL94V-0	DIP HOUSING	⑧
1	THERMOPLASTIC UL94V-0	SMT HOUSING	⑦
1	THERMOPLASTIC UL94V-0	LATCH HOUSING	⑥
12	COPPER ALLOY	DIP CONECTOR	⑤
12	COPPER ALLOY	SMT CONECTOR	④
2	SUS	EMI	③
1	SUS	LATCH	②
1	SUS	SHELL	①
QTY'S	MATERIAL	NAME	NO.

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/04/13	PART NAME: USB 3.1 TYPE C Gen 2 Female R/A, CL 1.73
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2015/04/13	PART NO. MUSB12-01-104
SCALE: 1:1 SIZE: A4		APPROVED BY1: Richard Hsieh	DATE : 2015/04/13	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2015/04/13	DRAW NO. SHEET NO.
				1 OF 2



GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/04/13	Phebe Su



USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuraation Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/04/13	PART NAME: USB 3.1 TYPE C Gen 2 Female R/A, CL 1.73
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2015/04/13	PART NO. MUSB12-01-104
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2015/04/13	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2015/04/13	DRAW NO. SHEET NO. 2 OF 2