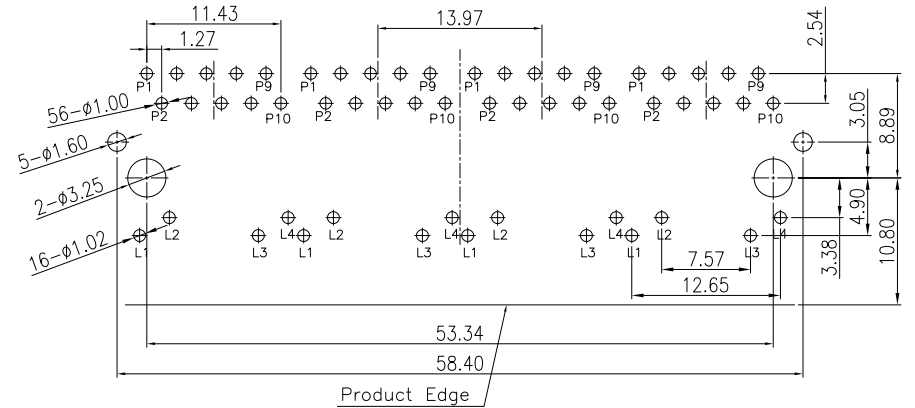
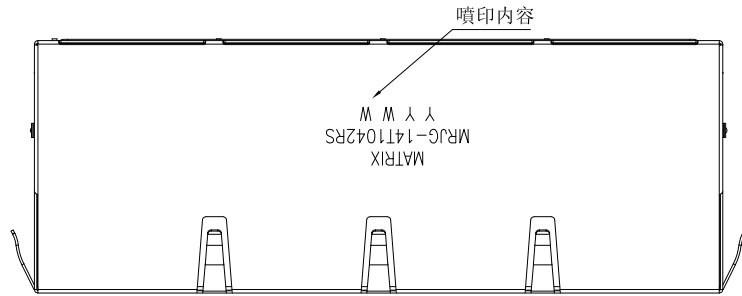


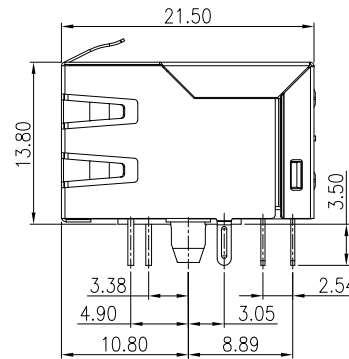
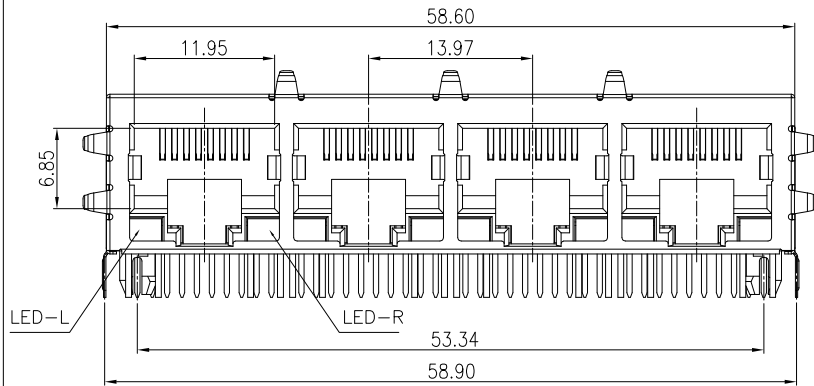
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2019/07/17	Phebe Su
A1			Optimized molding	2019/07/24	Phebe Su

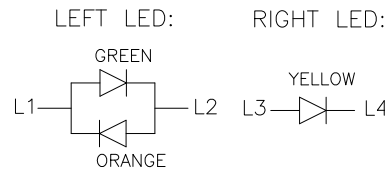
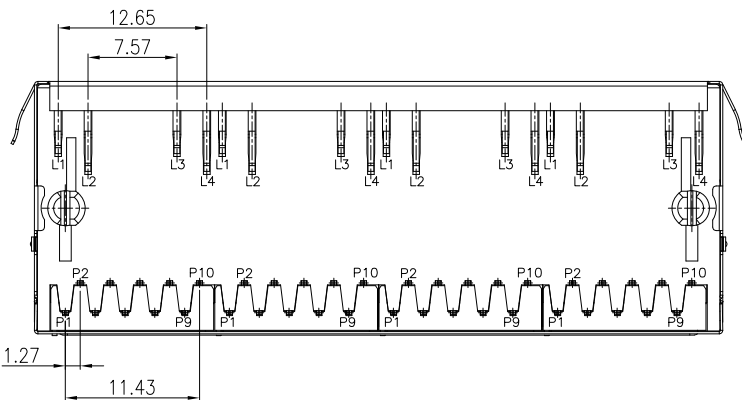
## 1. MECHANICAL DIMENSIONS:



SUGGESTED PCB LAYOUT  
DIMENSIONAL TOLERANCE: .XX ±0.05[0.002]  
(TOP VIEW)



- NOTES:
- MATERIALS:  
HOUSING -LCP UL94V-0 COLOR-BLACK.  
CONTACTS -0.35mm THICK PHOS-BRONZE PLATED WITH 6u' GOLD AND TIN IN SOLDER AREA.  
SHIELD - STAINLESS STEEL PLATED WITH NICKEL.
  - CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68,SUBPART F.

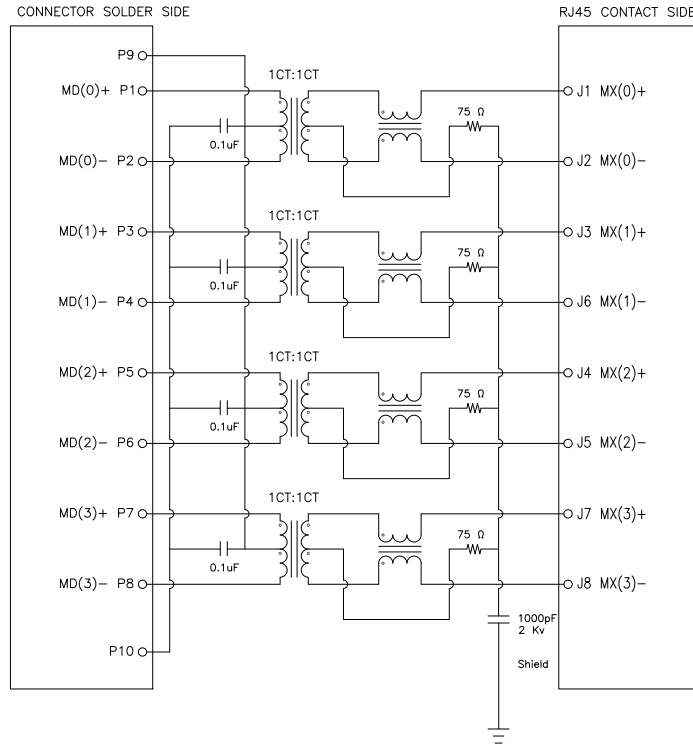


Matrix Electronics Co.,Ltd			
TOLERANCE: X:X X:XX X:XXX ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2019/07/24	PART NAME: RJ45 1X4,CONNECTORS,W/LED10P,8C SHIELDED100/1000Mbps,FILTER
UNIT: mm [inch]	CHECKED BY: Yoyo Yeh	DATE : 2019/07/24	PART NO. MRJG-14T1042RS
SCALE:1:1    SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2019/07/24	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2019/07/24	DRAW NO. SHEET NO. 1 OF 3

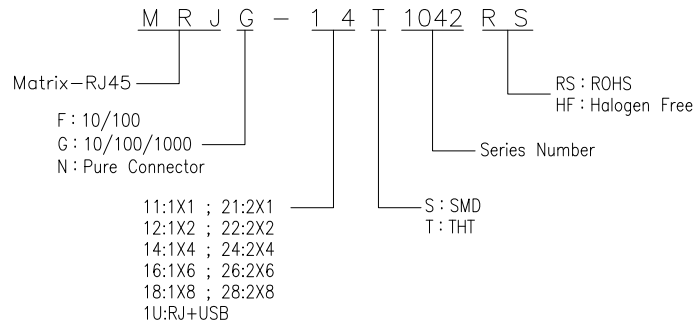
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2019/07/17	Phebe Su
A1			Optimized molding	2019/07/24	Phebe Su

## 2. SCHEMATIC:



MATRIX PART NO:



## 3. ELECTRICAL CHARACTERISTICS:

TEST NOTES:(25°±5°C)

- TR:(100KHz,1V); 100%  
 PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%  
 PINS:(P5,P6):(J4,J5)=1:1±3%;(P7,P8):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%  
 PINS:(P1,P2),(P3,P4),(P5,P6),(P7,P8)=350uH MINIMUM
- DCR: 100%  
 PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.2 Ω MAXIMUM
- HIPOT: 100%  
 PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1500VAC OR 2250VDC FOR 2 SECONDS  
 PINS(P5,P6)TO(J4,J5),(P7,P8)TO(J7,J8)=1500VAC OR 2250VDC FOR 2 SECONDS
- INSERTION LOSS:  
 -1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:  
 -18dB MINIMUM AT 1MHz TO 30MHz;  
 -16dB MINIMUM AT 30MHz TO 60MHz  
 -12dB MINIMUM AT 60MHz TO 80MHz  
 -10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:  
 -30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:  
 -30dB MINIMUM AT 1MHz TO 100MHz

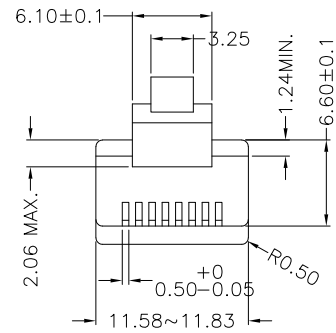


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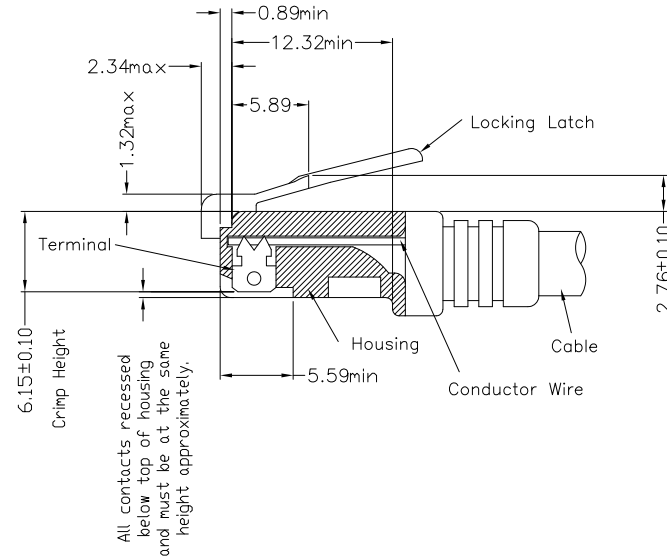
<b>TOLERANCE:</b> X: X X.X: ±0.38 X.XX: ±0.25 X.XXX: ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Phebe Su	<b>DATE :</b> 2019/07/24	<b>PART NAME:</b> RJ45 1X4,CONNECTORS,W/LED10P,8C SHIELDED100/1000Mbps,FILTER
	<b>CHECKED BY:</b> Yoyo Yeh	<b>DATE :</b> 2019/07/24	<b>PART NO.</b> MRJG-14T1042RS
 <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2019/07/24	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2019/07/24	<b>DRAW NO.</b> <b>SHEET NO.</b> 2 OF 3

# GP Component

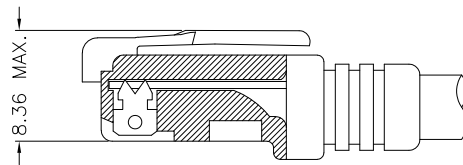
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2019/07/17	Phebe Su



- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)



Matrix Electronics Co.,Ltd

<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY :	DATE :	<b>PART NAME:</b> RJ45 1X4,CONNECTORS,W/LED10P,8C SHIELDED100/1000Mbps,FILTER
	Phebe Su	2019/07/24	
 UNIT: mm [inch]	CHECKED BY:	DATE :	PART NO.
	Richard Hsieh	2019/07/24	MRJG-14T1042RS
SCALE:1:1 SIZE:A4	APPROVED BY1:	DATE :	MOLD NO.
	Richard Hsieh	2019/07/24	NA
	APPROVED BY2:	DATE :	DRAW NO.
	Richard Hsieh	2019/07/24	SHEET NO.
			3 OF 3