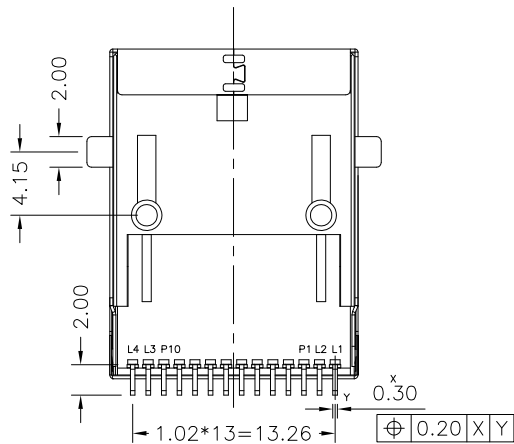
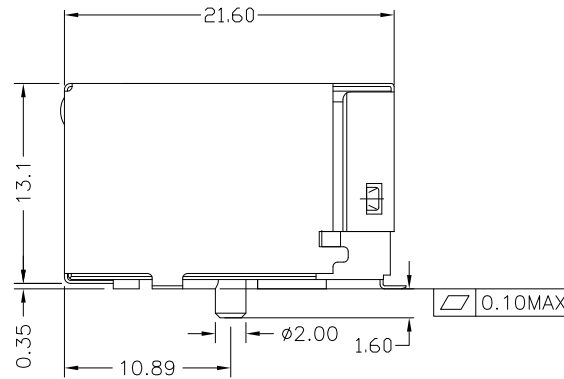
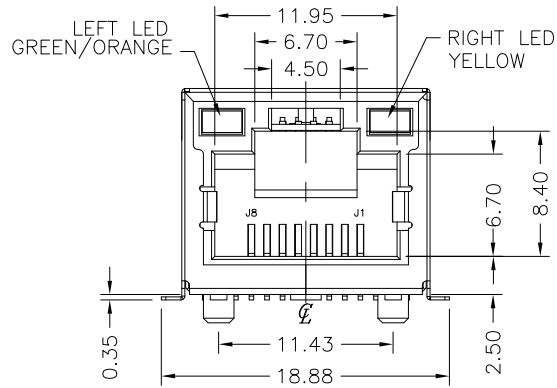
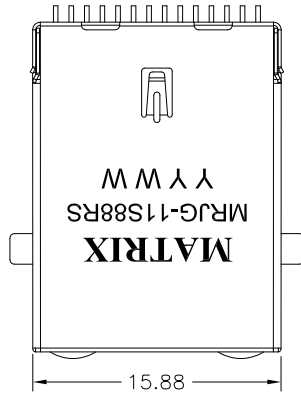


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie

1. MECHANICAL DIMENSIONS :



MATERIALS:

HOUSING: HIGH TEMPERATURE UL94V-0 BLACK
 CONTACTS: COPPER, 30u" NICKEL UNDERPLATE ON ALL AREA, 100u" TIN ON TERMINATION END, GOLD FLASH ON CONTACT AREA.

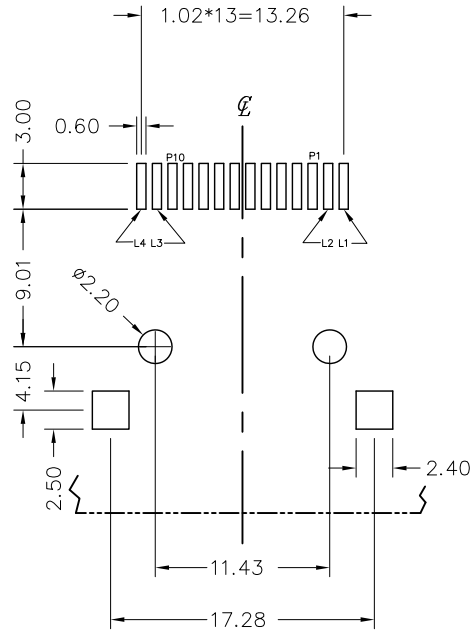
SHIELD: COPPER
 NICKEL 50u" MIN PLATING.

NO	VARIETY	QTY	MATERIAL	REMARK
			Matrix Electronics Co.,Ltd	
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Jamie Zie	DATE : 2013/9/26	PART NAME: 1x1RJ45 W/LED TAB UP
		CHECKED BY: Sam Kuo	DATE : 2013/9/26	PART NO. MRJG-11S8RS
		APPROVED BY1: Richard Hsieh	DATE : 2013/9/26	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2013/9/26	DRAW NO. SHEET NO. 1 OF 4
UNIT: mm [inch] SCALE: 1:1 SIZE: A4				

GP Component

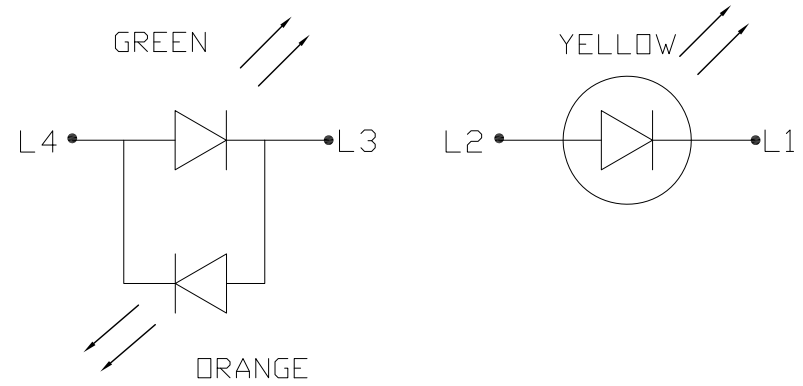
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie



2. PCB LAYOUT :



SUGGESTED PCB LAYOUT
(TOP VIEW)
DIMENSIONAL TOLERANCE: ±0.05

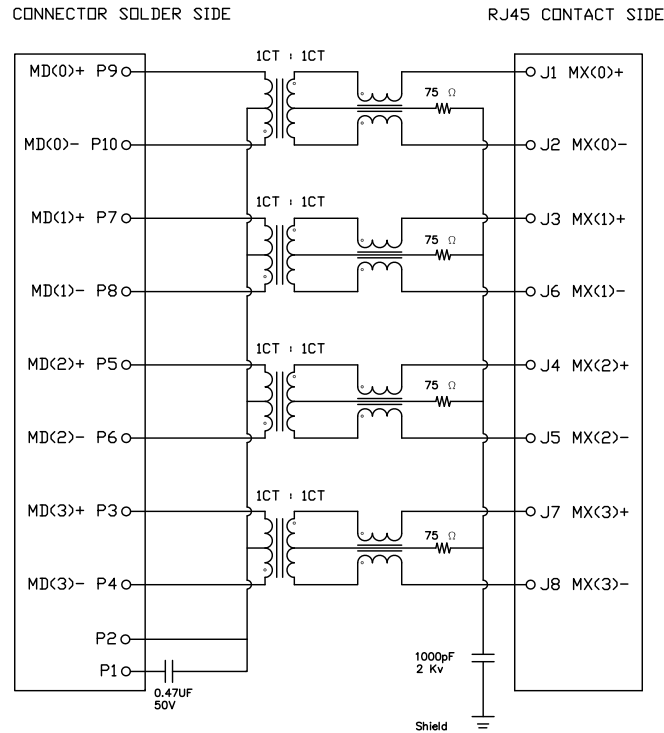
3. LED CONFIGURATION :



NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE:		DESIGN BY :	DATE :	PART NAME:
X.X	±0.38	Jamie Zie	2013/9/26	1x1RJ45 W/LED TAB UP
X.XX	±0.25	CHECKED BY:	DATE :	PART NO.
X.XXX	±0.13	Sam Kuo	2013/9/26	MRJG-11S88RS
ANGLE: ±3°		APPROVED BY1:	DATE :	MOLD NO.
		Richard Hsieh	2013/9/26	NA
UNIT: mm [inch]		APPROVED BY2:	DATE :	DRAW NO.
SCALE:1:1	SIZE:A4	Richard Hsieh	2013/9/26	SHEET NO.
				2 OF 4

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie

4. SCHEMATIC:

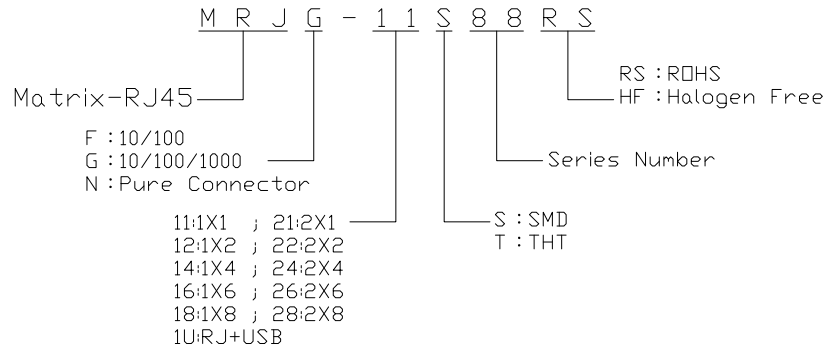



5. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P9,P10):(J1,J2)=1:1±3%;(P7,P8):(J3,J6)=1:1±3%
PINS:(P5,P6):(J4,J5)=1:1±3%;(P3,P4):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P9,P10),(P7,P8)=350uH MINIMUM
PINS:(P5,P6),(P3,P4)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1Ω MAXIMUM
- HIPOT: 100%
PINS(P9,P10)TO(J1,J2)=1500VAC OR 2250VDC FOR 2 SECONDS
PINS(P7,P8)TO(J3,J6)=1500VAC OR 2250VDC FOR 2 SECONDS
PINS(P5,P6)TO(J4,J5)=1500VAC OR 2250VDC FOR 2 SECONDS
PINS(P3,P4)TO(J7,J8)=1500VAC OR 2250VDC FOR 2 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-20dB MINIMUM AT 1MHz TO 30MHz;
-15dB MINIMUM AT 30MHz TO 60MHz
-10dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

MATRIX PART NO:

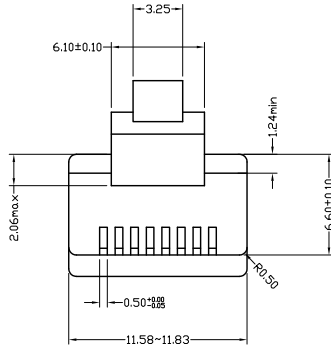


NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE:		DESIGN BY :	DATE :	PART NAME:
X : X X.X : ±0.38 X.XX : ±0.25 X.XXX : ±0.13 ANGLE: ±3°		Jamie Zie	2013/9/26	1x1RJ45 W/LED TAB UP
		CHECKED BY:	DATE :	PART NO.
		Sam Kuo	2013/9/26	MRJG-11S88RS
		APPROVED BY1:	DATE :	MOLD NO.
		Richard Hsieh	2013/9/26	NA
		APPROVED BY2:	DATE :	DRAW NO.
		Richard Hsieh	2013/9/26	
SCALE:1:1	SIZE:A4			SHEET NO.
				3 OF 4

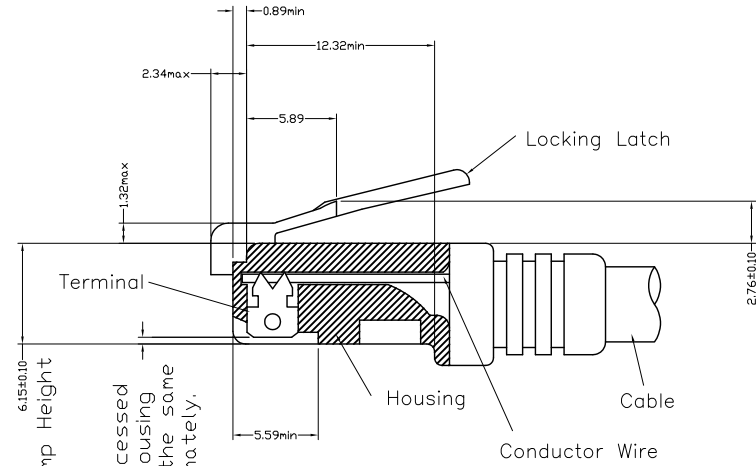
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie

5. 水晶頭尺寸:



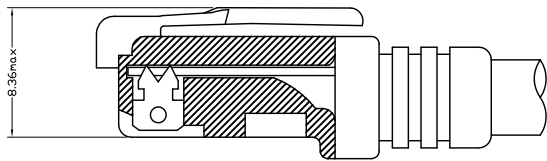
- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally





All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE:		DESIGN BY :	DATE :	PART NAME:
X.X	±0.38	Jamie Zie	2013/9/26	1x1RJ45 W/LED TAB UP
X.XX	±0.25	CHECKED BY:	DATE :	PART NO.
X.XXX	±0.13	Sam Kuo	2013/9/26	MRJG-11S88RS
ANGLE: ±3°		APPROVED BY1:	DATE :	MOLD NO.
		Richard Hsieh	2013/9/26	NA
UNIT: mm [inch]		APPROVED BY2:	DATE :	DRAW NO.
SCALE:1:1	SIZE:A4	Richard Hsieh	2013/9/26	SHEET NO.
				4 OF 4